

## CPLD package description

1.This package includes

- i.CAN-CPLD.zip -IspLever project (output file isa2can.vme)
- ii.ISPVM.exe +isa2can.vme -DOS based utility for on board programming of CPLD.
- iii.CLPD-readme

2.To program device user should put 4 jumpers:

- P18-3 & P18-4
- P18-5 & P18-6
- P18-7 & P18-8
- P18-9 & P18-10

and run ispvme.exe with argument isa2can.vme.(SB-iGLX is sold programmed)

3.The programmed .I/O space for CAN bus is 4-byte size(actually used only 2 bytes) from 330H.

The operation is divided into two steps:

1. Writing address of register into address 330H
2. Reading/writing content from/to address 331H.

4.Proposed can bus implementation is similar to “CAN adapter for ISA Bus” published at Elektor

Electronics magazine 12/2000 , so user should read this article for more information.

you should buy it from the elektor page (file E00C012.pdf)

<http://www.elektor.com/default.lynkx?pointer=1-28-16120-16309-16320-25900>

### Update 09092009

1.CAN-CPLD project was updated

1. IRQ was changed to 5
2. IRQ inverter was removed